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RF CMOS - Wikipedia

The simple diode varactor circuit is adopted for center-frequency tuning. These two improved circuits can reduce the power dissipation of the amplifier. An experimental chip fabricated by 0.5 μm double-poly-double-metal CMOS technology occupies a chip area of 500 × 500 μm² chip area. The measured results have verified the performance of the ...

The design of a 2-V 900-MHz CMOS bandpass amplifier ...

The project for this class will involve the design and simulation of a down-conversion chain for a radio frequency receiver in a 0.18 m CMOS process. The project will be split into three subprojects: the rst will be to design a low noise ampli er (LNA), the second will be to design a mixer, and the third will be to design a VCO and combine the three

ECE 6730: Radio Frequency Integrated Circuit Design

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The Design of CMOS Radio-Frequency Integrated Circuits

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